

Electronic Acknowledgement Receipt

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Application Number:	10580167
International Application Number:	
Confirmation Number:	3765
Title of Invention:	Am Intermediate Frequency Variable Gain Amplifier Circuit, Variable Gain Amplifier Circuit and its Semiconductor Integrated Circuit
First Named Inventor/Applicant Name:	Hiroshi Katsunaga
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Application Type:	U.S. National Stage under 35 USC 371

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1		5000-5310amnd.pdf	412454 a2913b89905dc5fbc3937c12d36d414c53628ca	yes	8
Multipart Description/PDF files in .zip description					
Document Description			Start	End	
Amendment - After Non-Final Rejection			1	1	
Claims			2	5	
Applicant Arguments/Remarks Made in an Amendment			6	8	
Warnings:					
Information:					
2	Extension of Time	5000-5310ext.pdf	92283 3167d200d2eb00e37bf8fed3ac270a8aeb7c5d49	no	2
Warnings:					
Information:					
3	Fee Worksheet (PTO-06)	fee-info.pdf	8221 4ec29ec4412131c521ca851dd0220927341e176	no	2
Warnings:					
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National Stage of an International Application under 35 U.S.C. 371

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